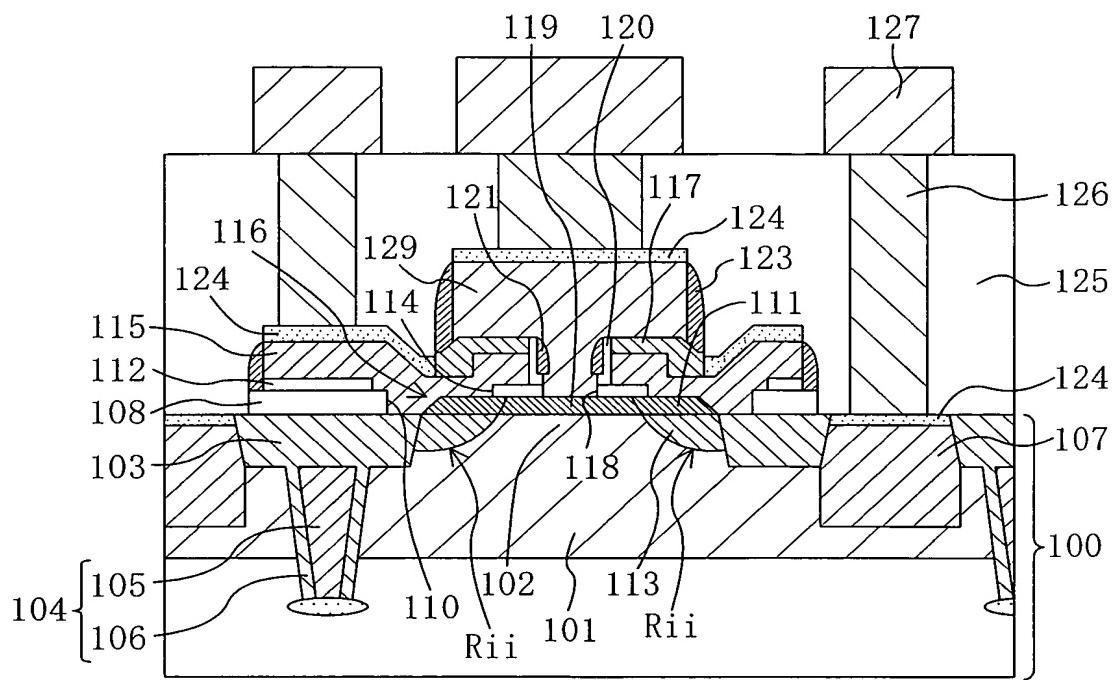


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FIG. 1



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FIG. 2(a)

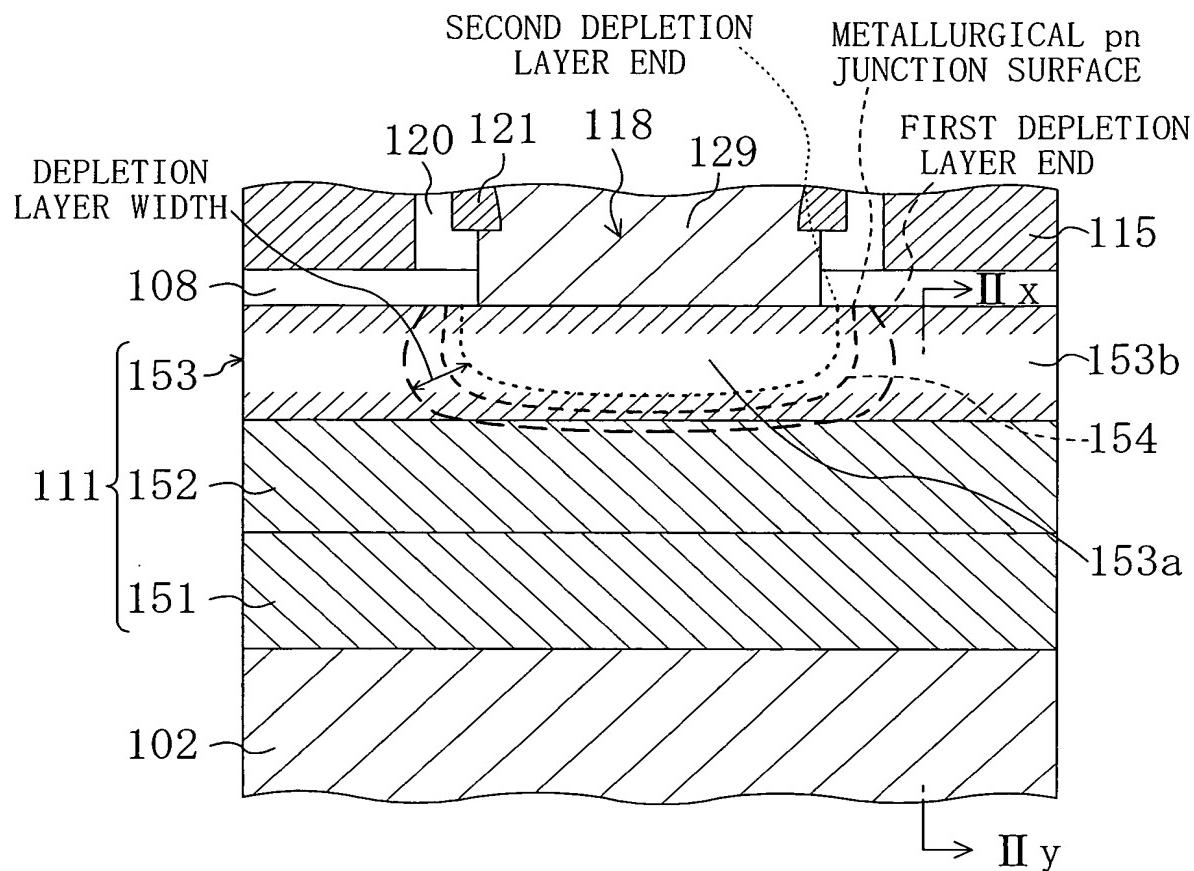
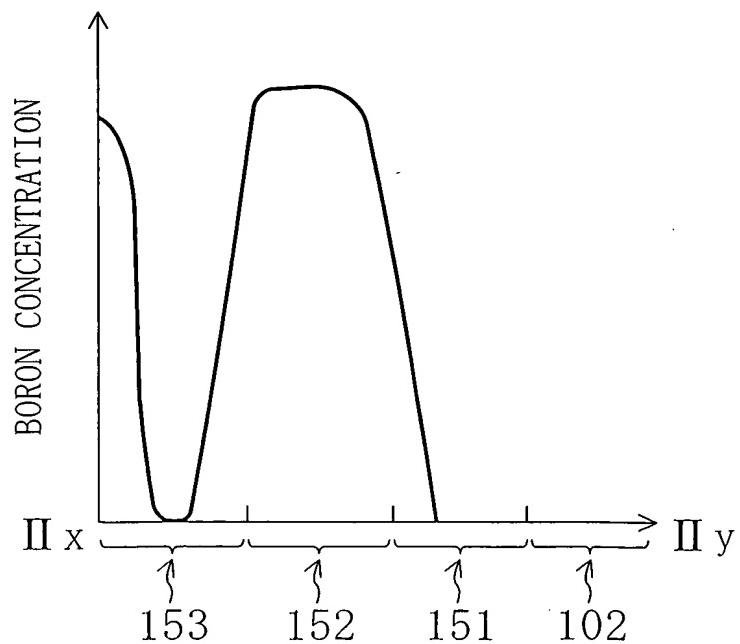


FIG. 2(b)



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FIG. 3(a)

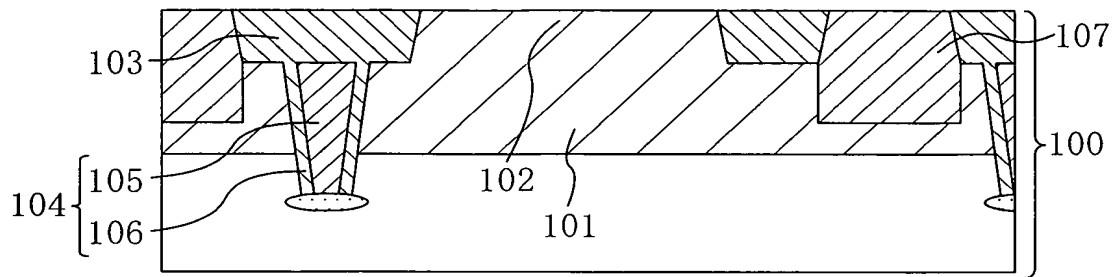
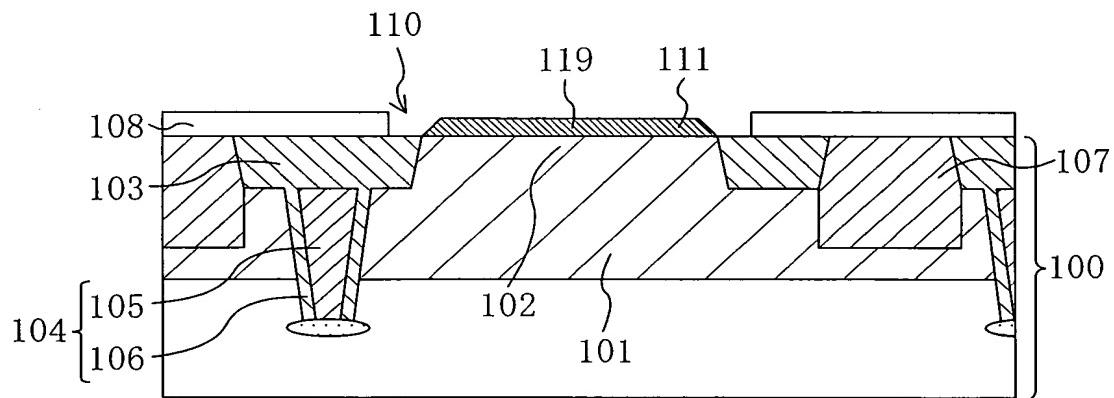


FIG. 3(b)



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FIG. 4(a)

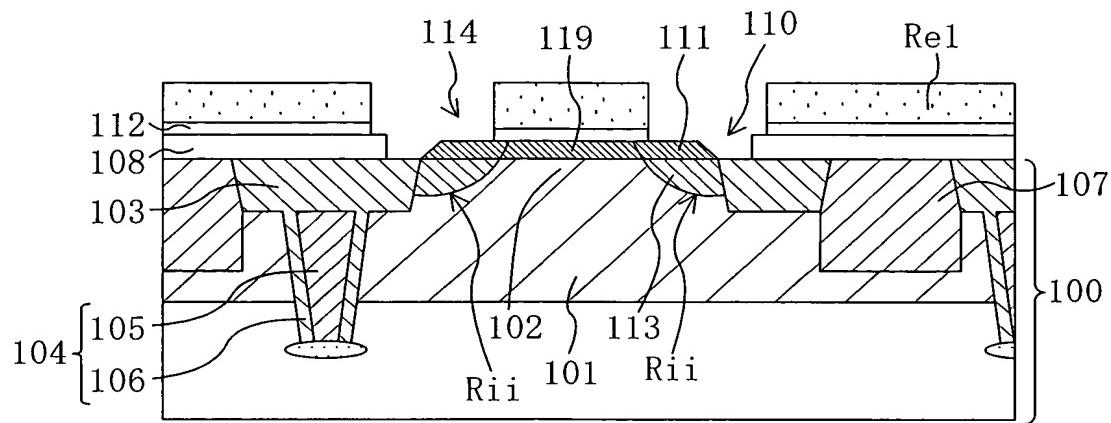


FIG. 4(b)

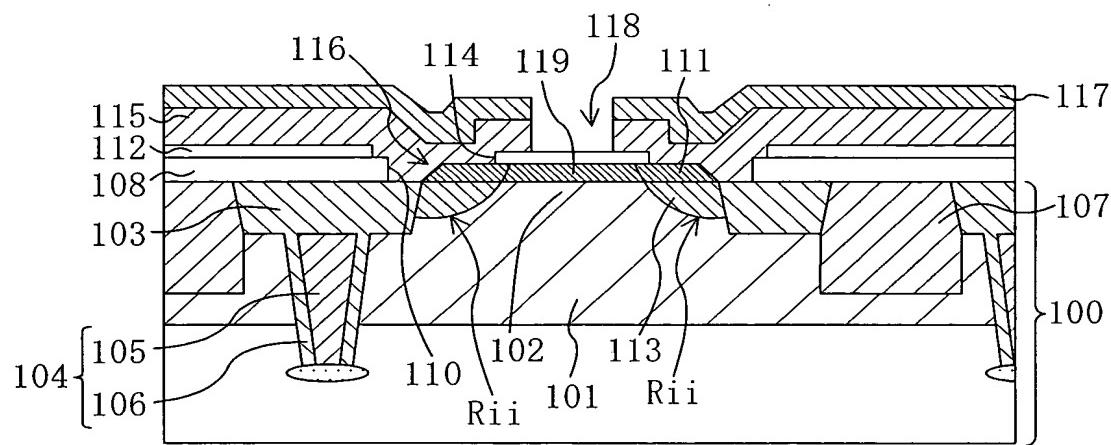


FIG. 5(a)

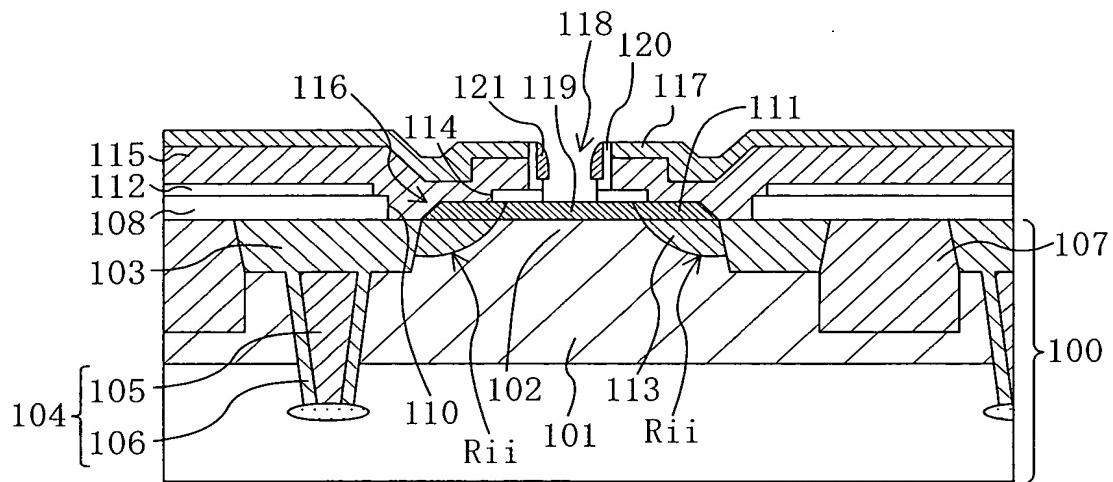
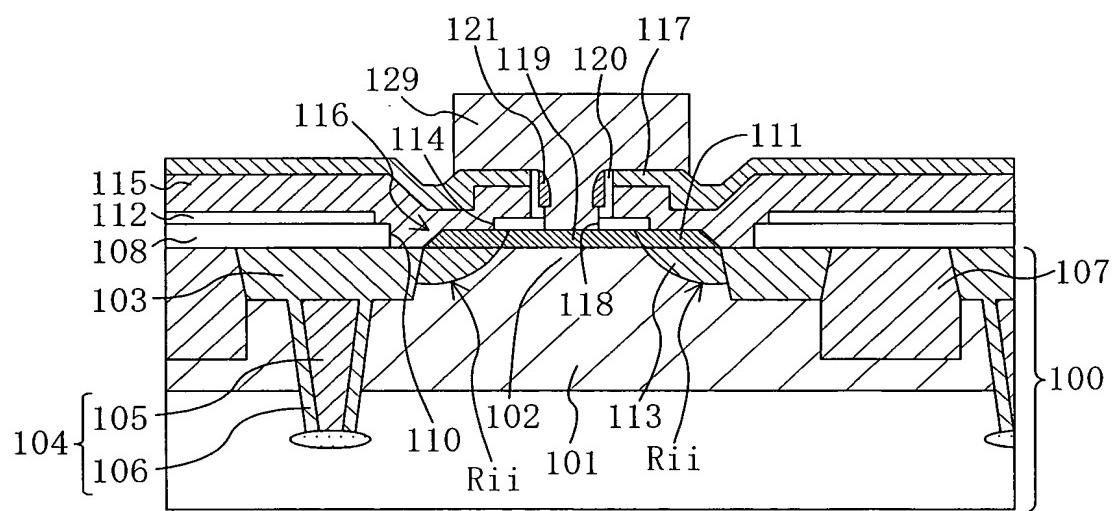


FIG. 5(b)



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FIG. 6(a)

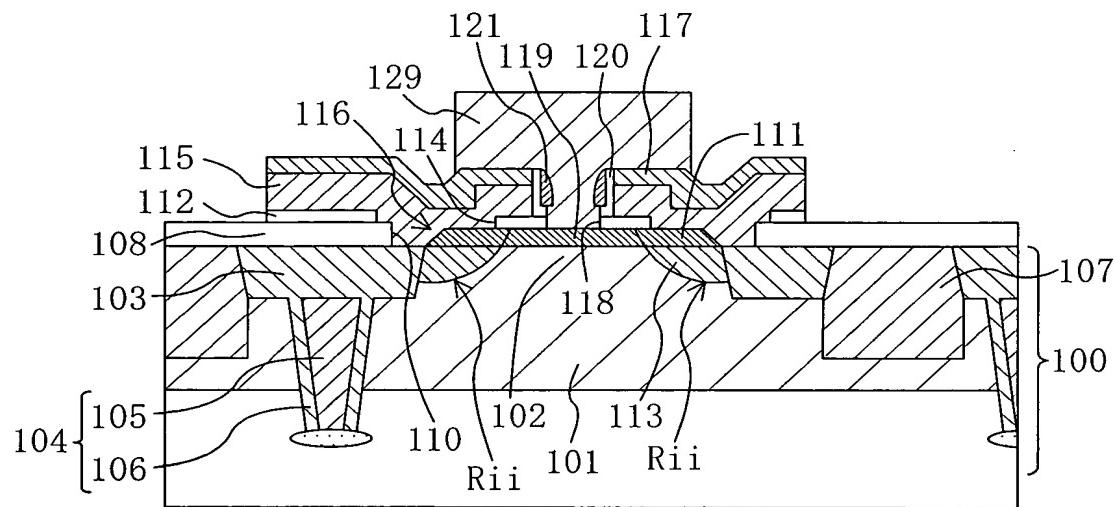


FIG. 6(b)

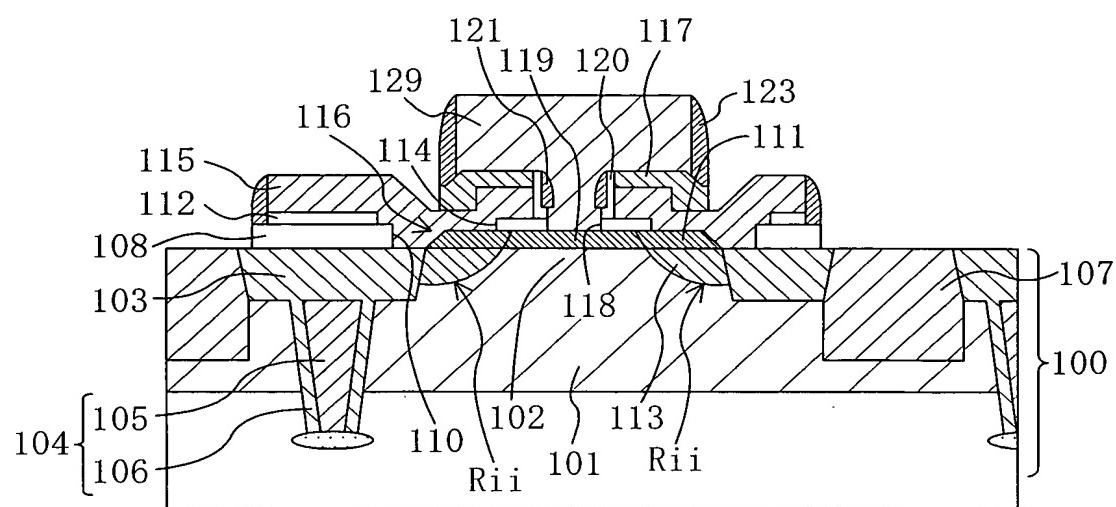


FIG. 7 (a)

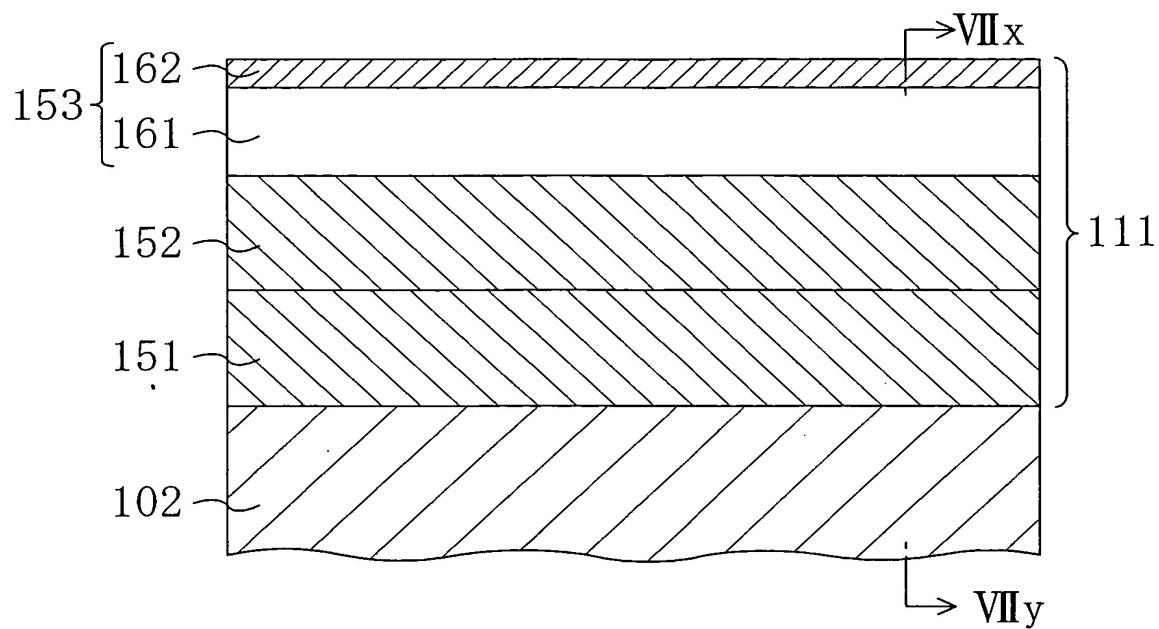
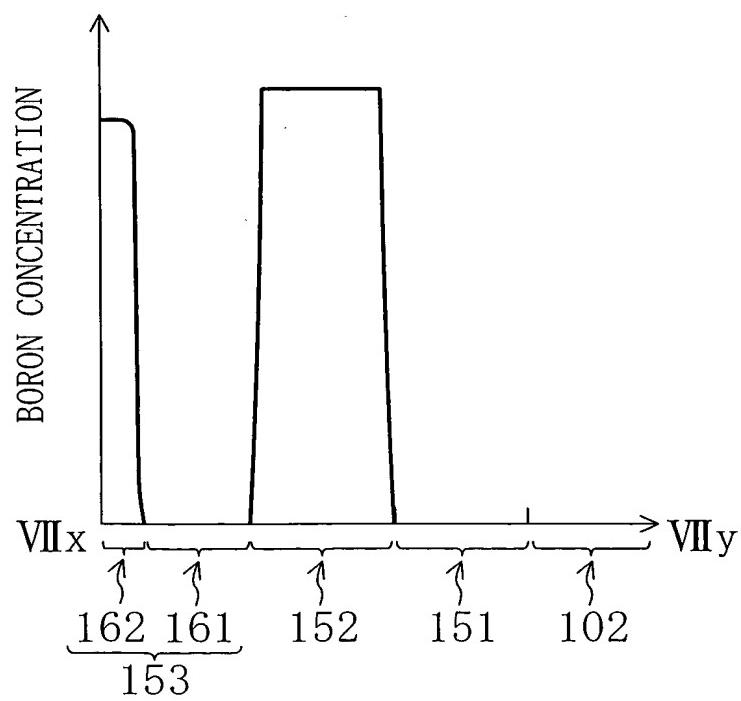


FIG. 7 (b)



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FIG. 8(a)

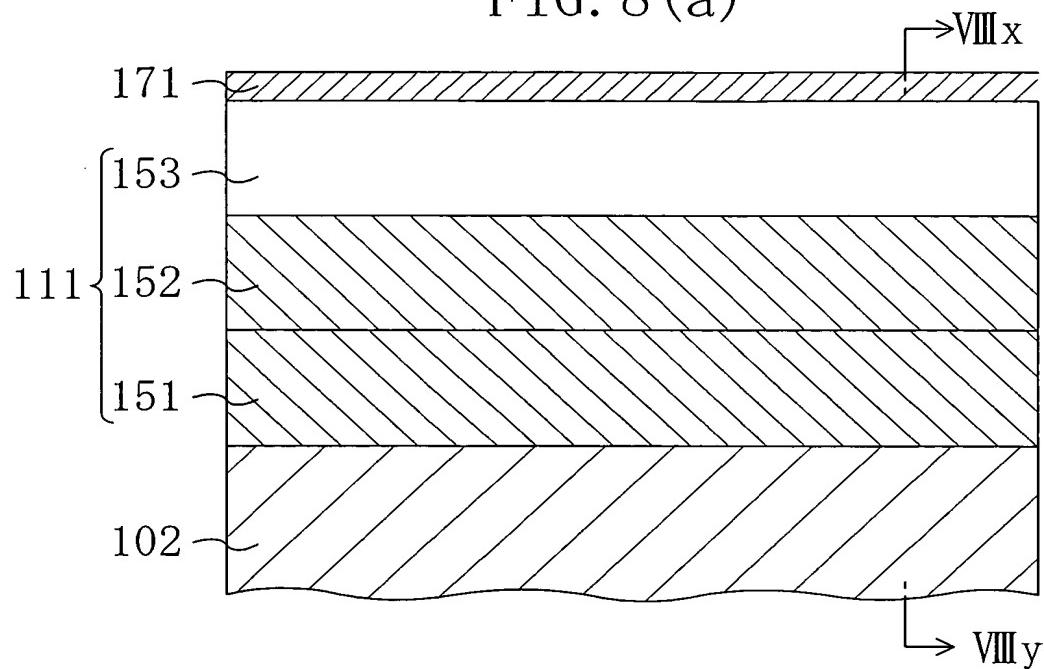
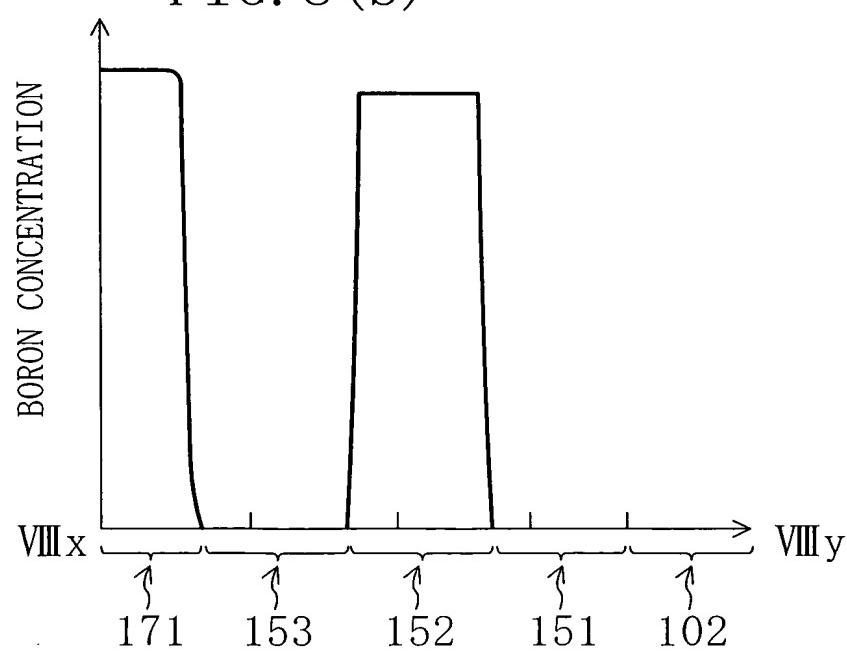


FIG. 8(b)



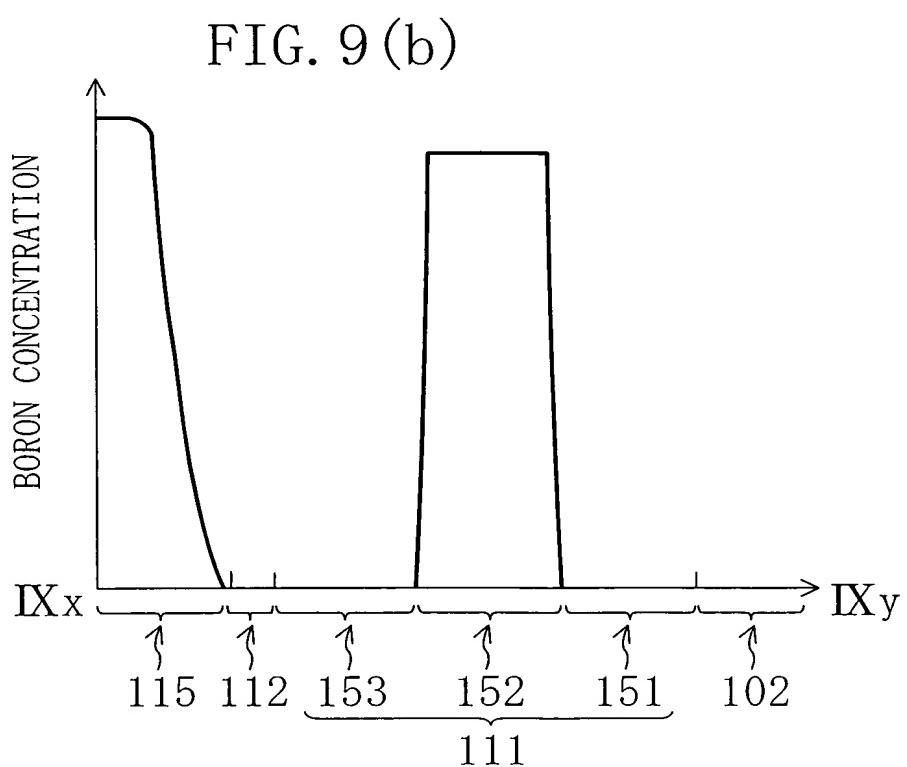
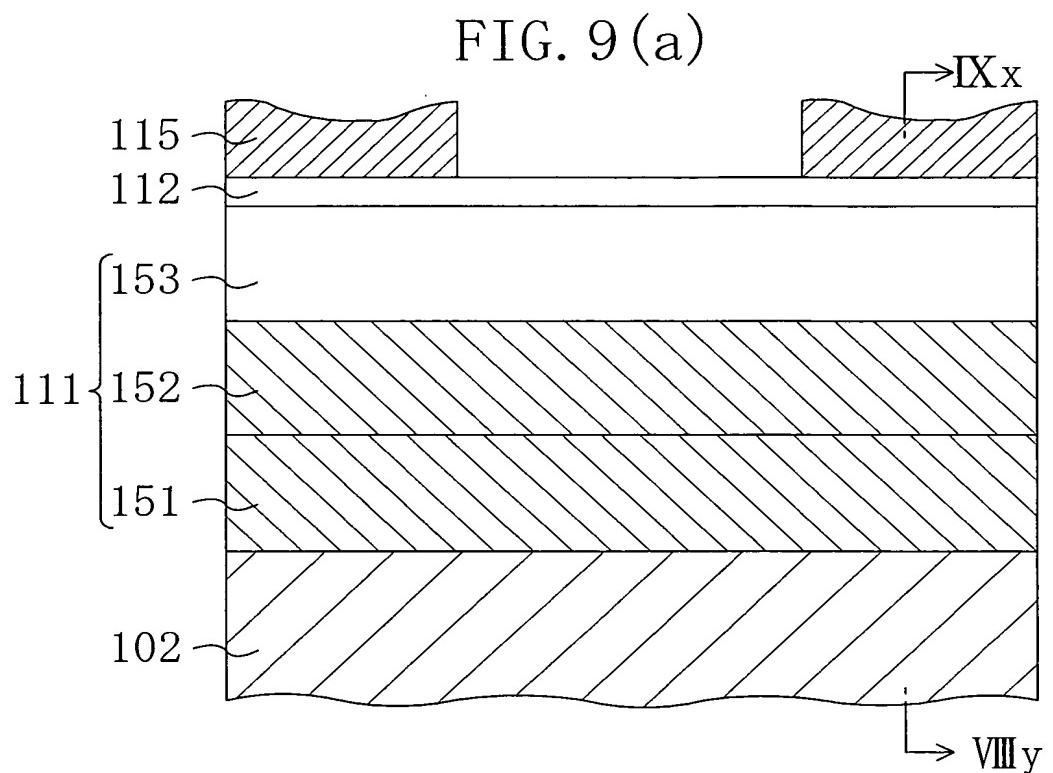
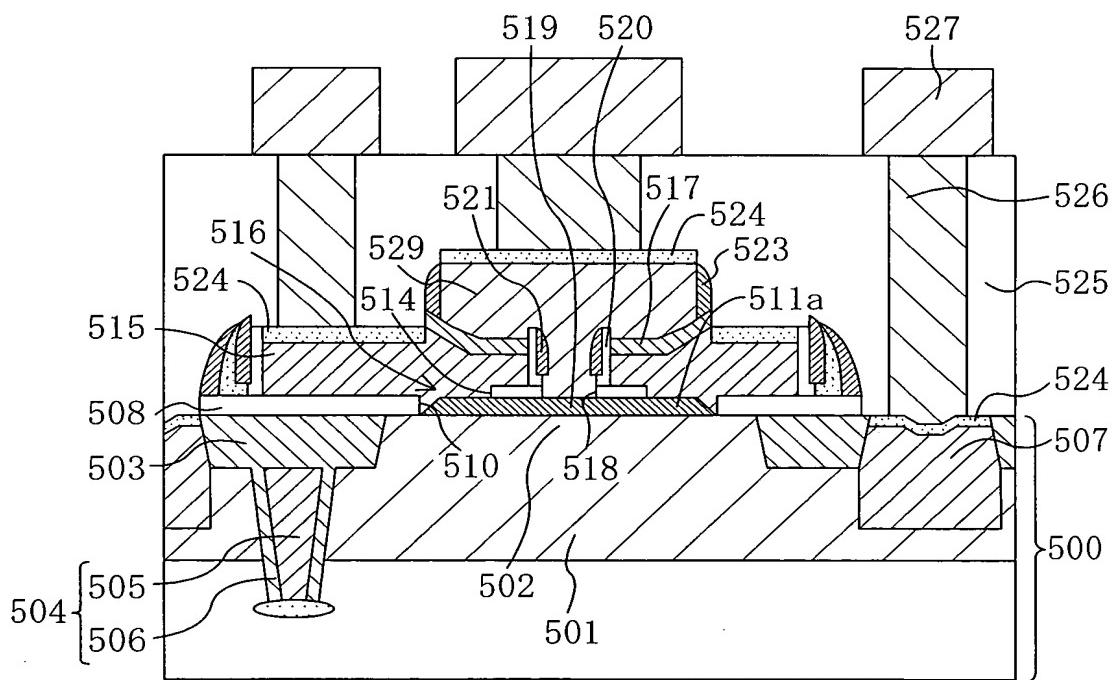


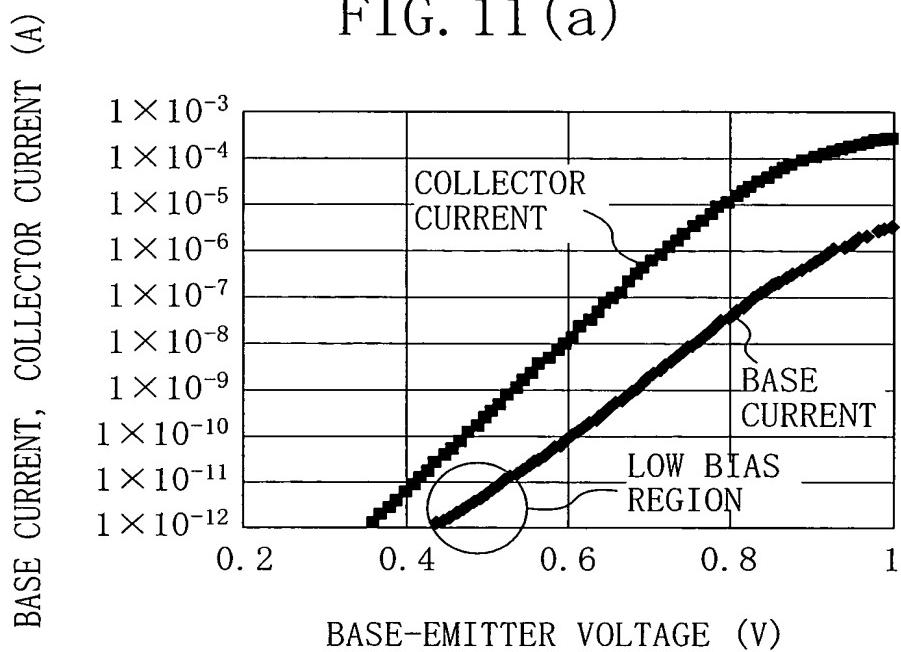
FIG. 10



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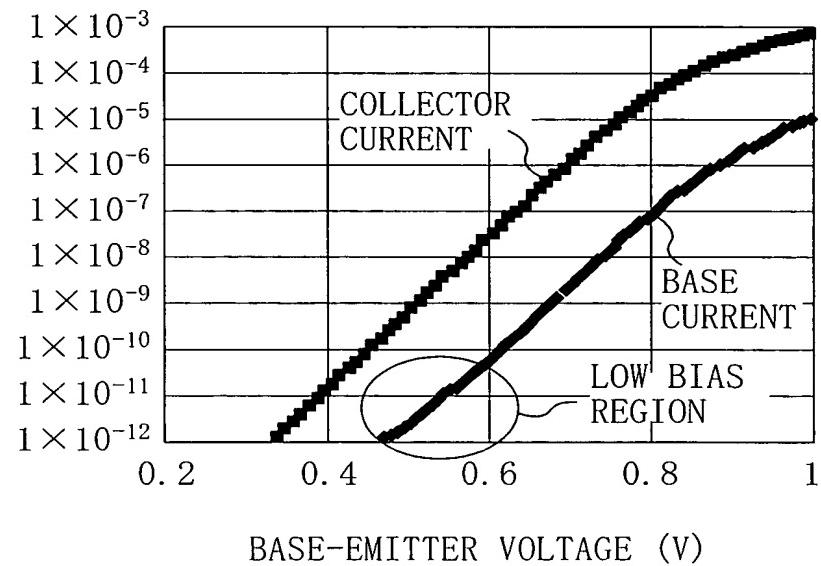
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FIG. 11 (a)



BASE CURRENT, COLLECTOR CURRENT (A)

FIG. 11 (b)



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FIG. 12(a)  
PRIOR ART

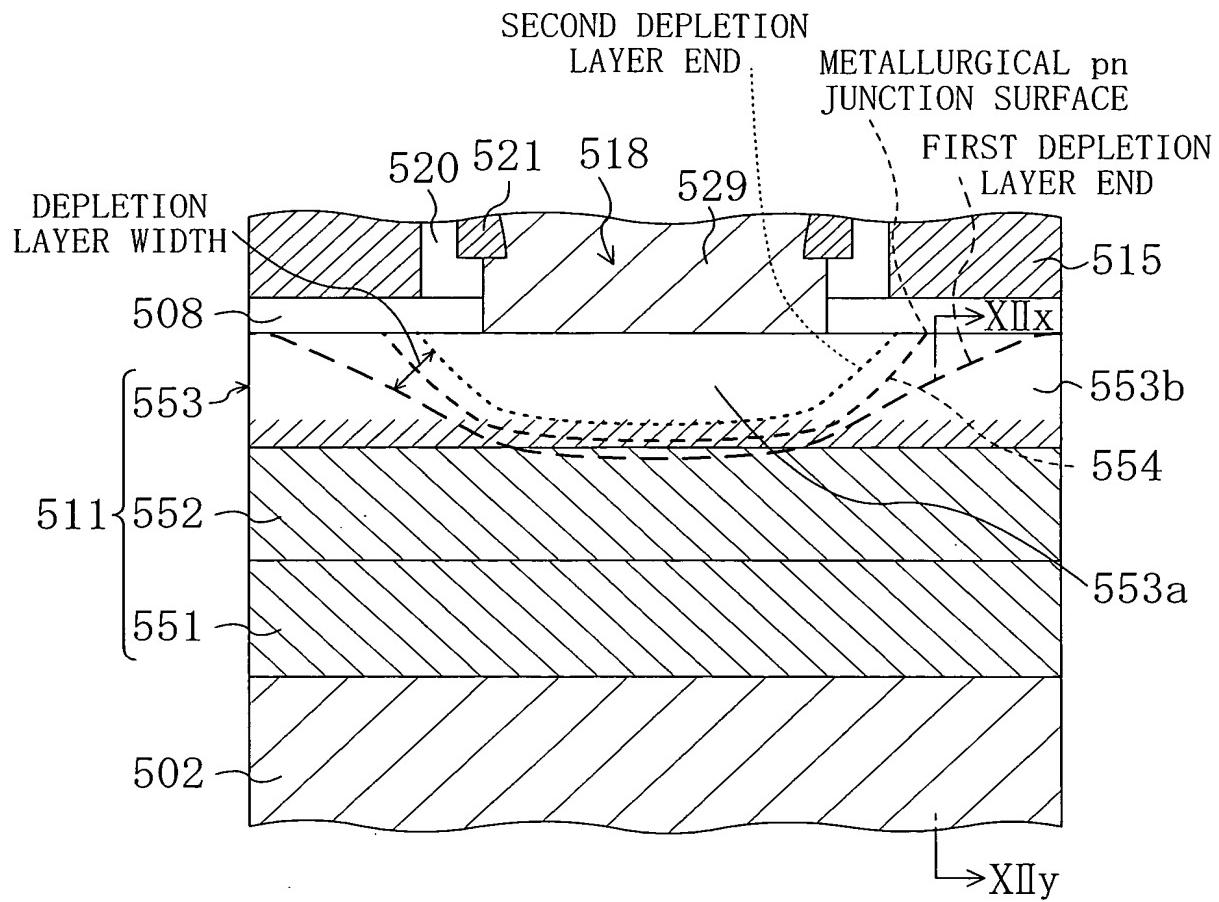


FIG. 12(b)  
PRIOR ART

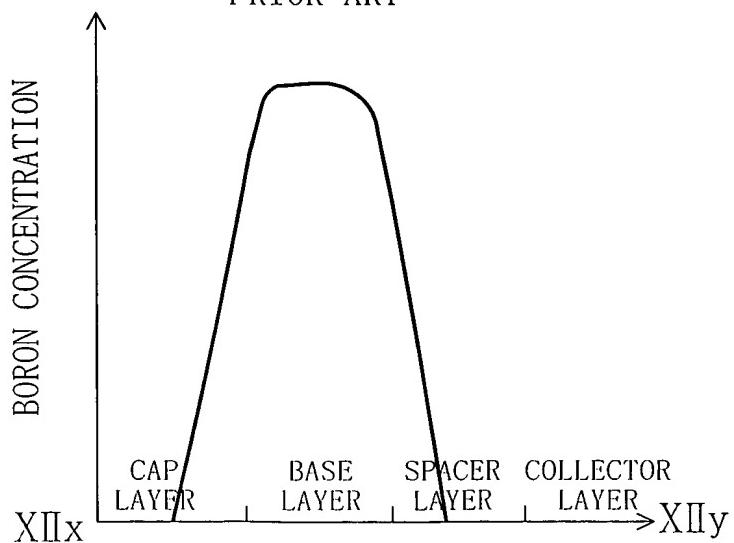


FIG. 13

